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T2 T3 T4 T5 T6 T7 T8 T9 T10					WB	EXE MEM WB
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15	WB	EXE MEM WB	EXE MEM WB	RD	<u>ц</u>	
7	MEM	EXE	RD	止		
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	INSTR. 1	INSTR. 2	INSTR. 3	INSTR. 4	INSTR. 5	INSTR. 6

Fig. 1

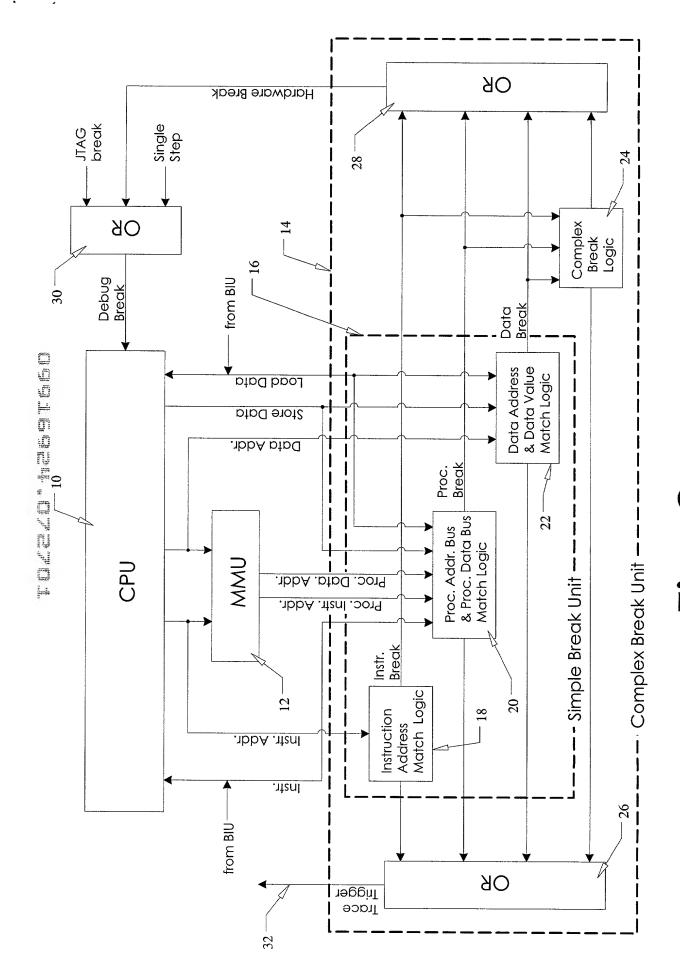


Fig. 2

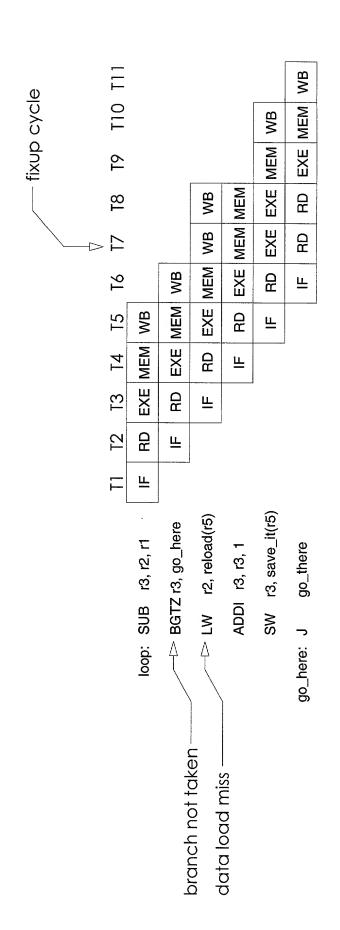


Fig. 3

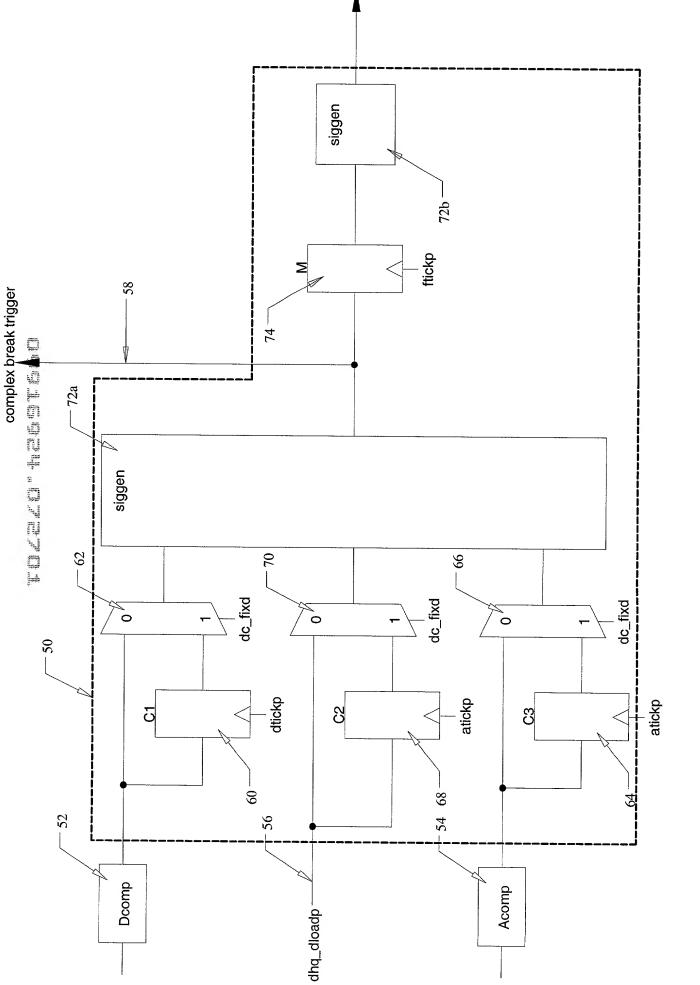


Fig. 4

